

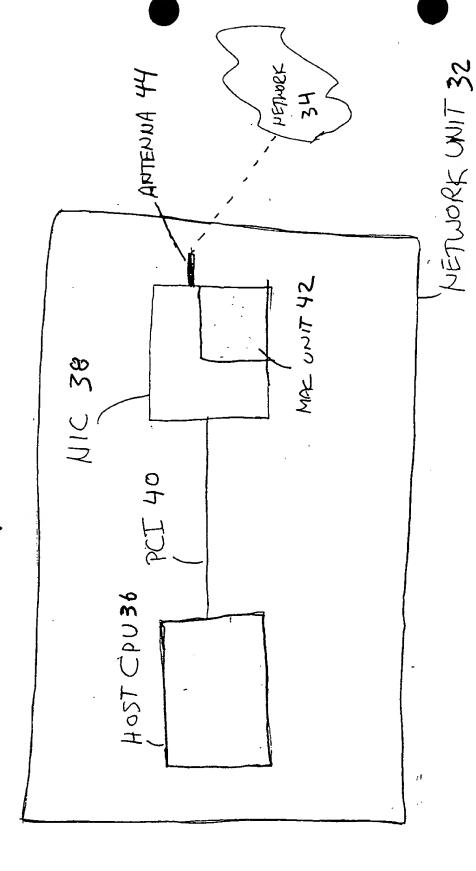
16

PRIOR ART.

HOST CPU .6 MLME 18 (HOST SW) FLPM 20 The first time the constitute of the state of (MAC SW) FTM 22 (MAC HW) MAC LAYER 16 PHY (HW) 24 NETWORK 8

PAGR ART

F1G, 2



F16, 3

HOST CPU 36 MLME (HOST SW) THE RESIDENCE OF THE PARTY OF T FINCTIONS (HOST SW) 50 the first from the way for the FTM SE (MAC HW) MAC LAYER PHY 58 46 (HW) NETWORK 34

FIG. 4

